Auto-sensing bidirectional voltage level translators

Over the last decade semiconductor CMOS technology has advanced to produce high-performance systems with lower voltage and lower power I/O standards, especially in portable applications. This led to confrontation amongst engineers with an increasing complexity of modern portable electronic systems in which low power and voltage logic levels are used. Incompatibility between I/O levels becomes prevalent if different interface voltages are required within the system allowing safe communication between multiple devices without damage due to over voltage or other reasons.

When older legacy peripherals are used in multi-voltage applications, these higher voltage devices need to be interfaced with the lower voltages of the latest application processors that are used widely today. Nexperia's LSF switch-type translators enable these voltage translating interfaces and can be used for both open-drain and push-pull buffered applications.

Overview

Nexperia's Voltage Level Translators offer lower voltage and excellent performance in pin-to-pin compatible packages with industry-standard footprints. The LSF family comprises of dual supply, bidirectional voltage level translators operational from 0.95 V to 5.5 V allowing translation without the need for a direction pin in open-drain and push-pull applications. The LSF family supports level translation applications with transmission speed greater than 100Mbps for open drain utilizing a 30pF capacitance and 2000hm pull-up resistor. Minimal propagation delay and signal distortion for the connections to be made due to low RON, assuming higher voltage is on the B-side port.

Applications

- > Open-drain applications
 - I2C Bus, SMBus
 - PMBus
- > UARTs, HDMI, 1-wire sensor Bus
- > GPIO and other telecom infrastructure
- > Bluetooth headsets
- Mobile and computing
- Communications systems
- > Enterprise Systems
- > Automotive



Key features

- Complete family comprising of 1,2,4 and 8-channel level translators
- > Easy to use Dual supply translator family
- Auto-sensing bidirectional voltage translation
 - no direction pin required
- **>** Low R_{ON} provides less signal distortion
- High speed Up translation
 - ≤ 100 MHz; CL = 30 pF
 - ≤ 40 MHz; CL = 50 pF
- > High speed Down translation
 - ≥ 100 MHz; CL = 30 pF
 - ≤ 40 MHz; CL = 50 pF
- > Supports hot insertion

- > Bidirectional voltage level translation between:
 - 0.95 V and 1.8 V, 2.5 V, 3.3 V and 5.0 V
 - 1.2 V and 1.8 V, 2.5 V, 3.3 V and 5.0 V
 - \bullet 1.8 V and 2.5 V, 3.3 V and 5.0 V
 - 2.5 V and 3.3 V and 5.0 V
 - 3.3 V and 5.0 V
- > Low standby current
- > 5 V tolerant I/O pins to support TTL
- > Low R_{ON} provides less signal distortion
- > High-impedance I/O pins for EN = Low.
- > Flow-through pinout for easy PCB trace routing.
- > Specified from -40 °C to +125 °C

Product	Description	V _{CC(A)} (V)	V _{CC(B)} (V)	T _{amb} (°C)	Type(s)
LSF0101	1-bit bidirectional multi-voltage level translator; open-drain; push-pull	0.95 – 5.0	0.95 – 5.0	-40 °C to +125 °C	LSF0101GM, LSF0101GX, LSF0101GW**
LSF0102	2-bit bidirectional multi-voltage level translator; open-drain; push-pull	0.95 – 5.0	0.95 – 5.0	-40 °C to +125 °C	LSF0102DP, LSF0102DC, LSF0102GS, LSF0102GX, LSF0102DC-Q100, LSF0102DP-Q100
LSF0204	4-bit bidirectional multi-voltage level translator; open-drain; push-pull	0.8 – 5.0	0.8 – 5.0	-40 °C to +125 °C	LSF0204GU12, LSF0204PW, LSF0204PW-Q100
LSF0108	8-bit bidirectional multi-voltage level translator; open-drain; push-pull	0.95 – 5.0	0.95 – 5.0	-40 °C to +125 °C	LSF0108PW, LSF0108BQ, LSF0108BQ-Q100, LSF0108PW-Q100

^{- **}In development

Packages

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Functional diagram	Pinning configuration	SOT#	Package suffix	Package name	No of pins	Dimensions (L × W, pitch - in mm)	Package
LSF0101	LSF0101 GND	SOT886	GM	XSON6	6	1 x 1.45, 0.5	No. of the second secon
refA refB A1 B1 EN aaa-030903	LSF0101 CND	SOT363	GW	TSSOP6	6	2.1 x 2, 0.65	
	CAD	SOT1255-2	GX	X2SON6	6	1 x 0.8, 0.8	

Packages

Functional diagram	Pinning configuration	SOT#	Package suffix	Package name	No of pins	Dimensions (L x W, pitch - in mm)	Package
	LSF0102 CAD T	SOT1233-2	GX	X2SON8	8	1.35 X 0.8, 0.5	
refA refB A1 B1 A2 B2	LSF0102 CND 1 8 EN refA 2 7 refB A1 3 6 B1 A2 4 5 92 sse431134	SOT1203	CS	XSON8	8	1.35 X 1, 0.55	
EN	CSO 1102 CSO 11 198 CSO 11 199 CSO 11 1	SOT505-2	DP	TSSOP8	8	4 X 3, 0.65	
	L5F0102 ORD 1	SOT765-1	DC	VSSOP8	8	3.1 x 2, 0.5	
LSF0204	LSF0204 refA 1	SOT402-1	PW	TSSOP14	14	6.4 x 5, 0.65	
An Bn to other channels aaa+031585	LSFOZD4 terminal LSFOZD4 terminal	SOT1174-1	GU12	XQFN12	12	2 x 1.7, 0.4	Lann June
LSF0108 refA refB A1 B1 A2 B2 A3 B3	Lerminal 1 \$\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	SOT764-1	BQ	DHVQFN20	20	2.5 x 4.5, 0.5	Signal Si
A5 B5 A6 B6 A7 B7 AB B8 EN aaa 6302280	LSF0108 GHD 1 refa 2 A1 1 A2 2 A3 2 A4 5 A5 7 A5 8 A5 7 A6 8 A6 10 A6 10 A6 10 A6 10 A7 10 A7 10 A8 10	SOT360-1	PW	TSSOP20	20	6.4 x 6.5, 0.65	

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